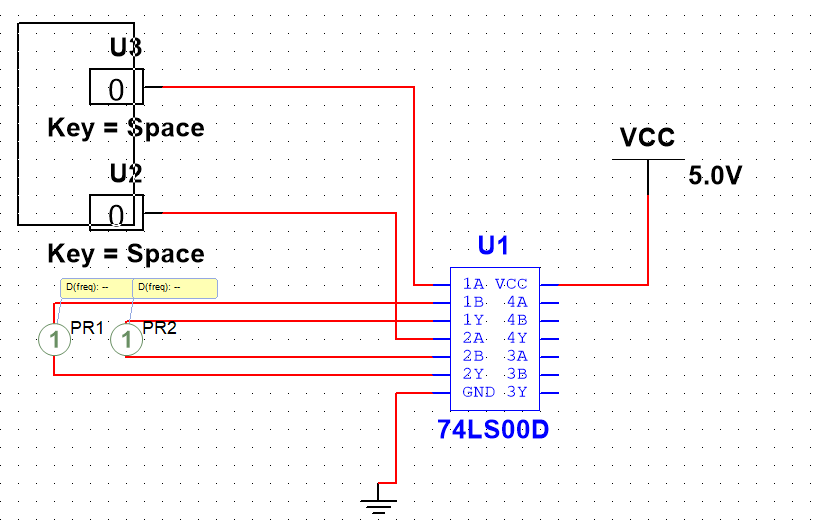
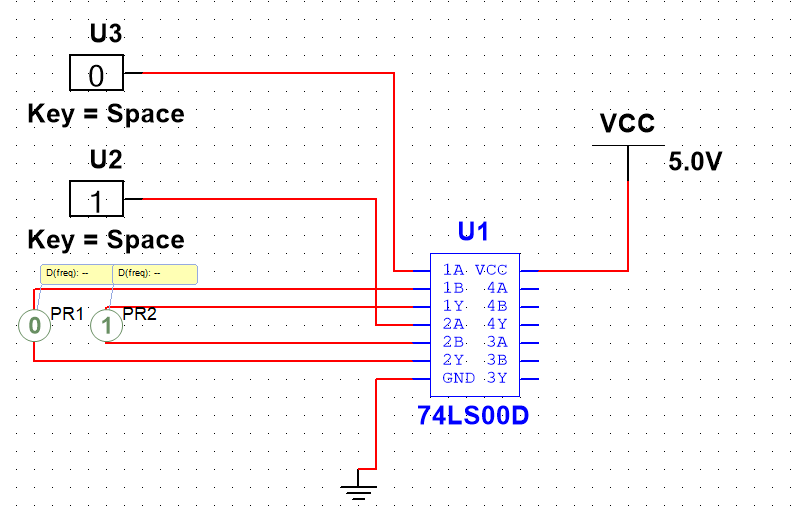
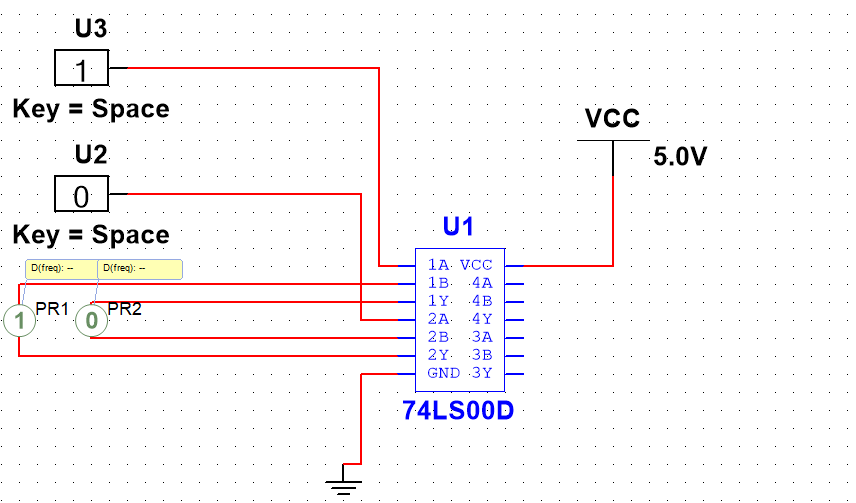
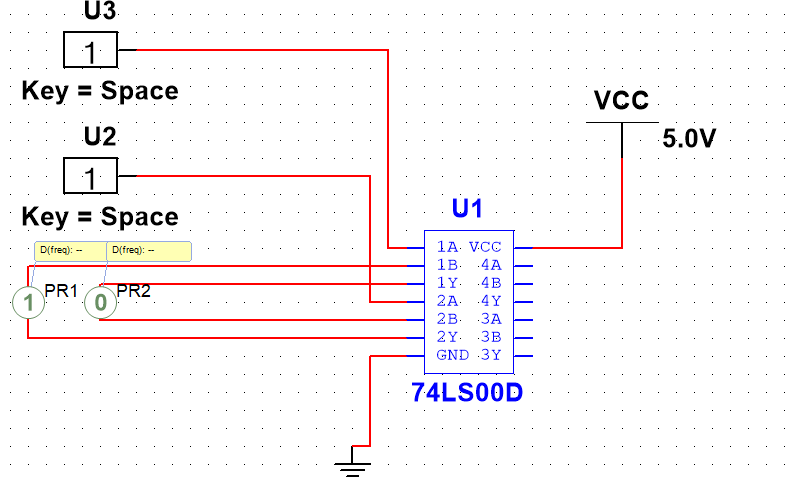
1. 学习用与非门（与非门为四2输入与非门74LS00）组成基本RS触发器并测试其逻辑功能。



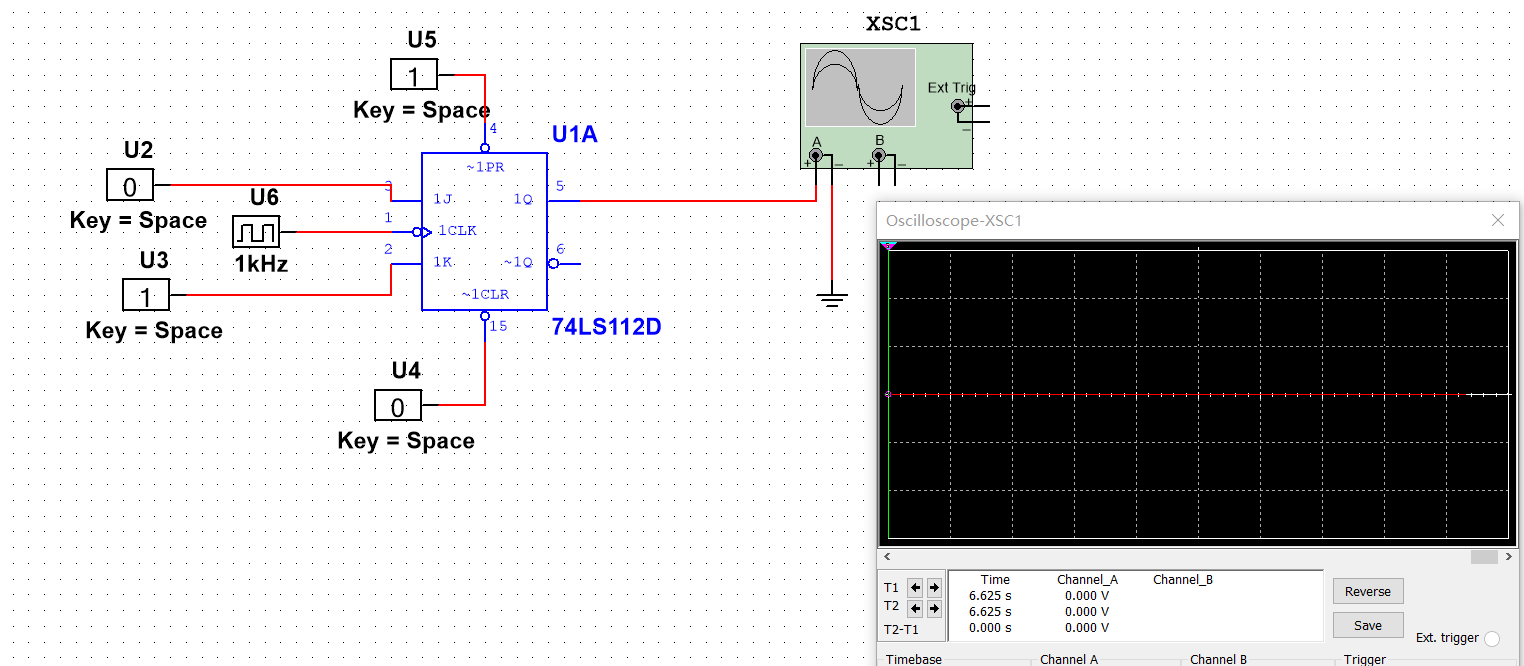


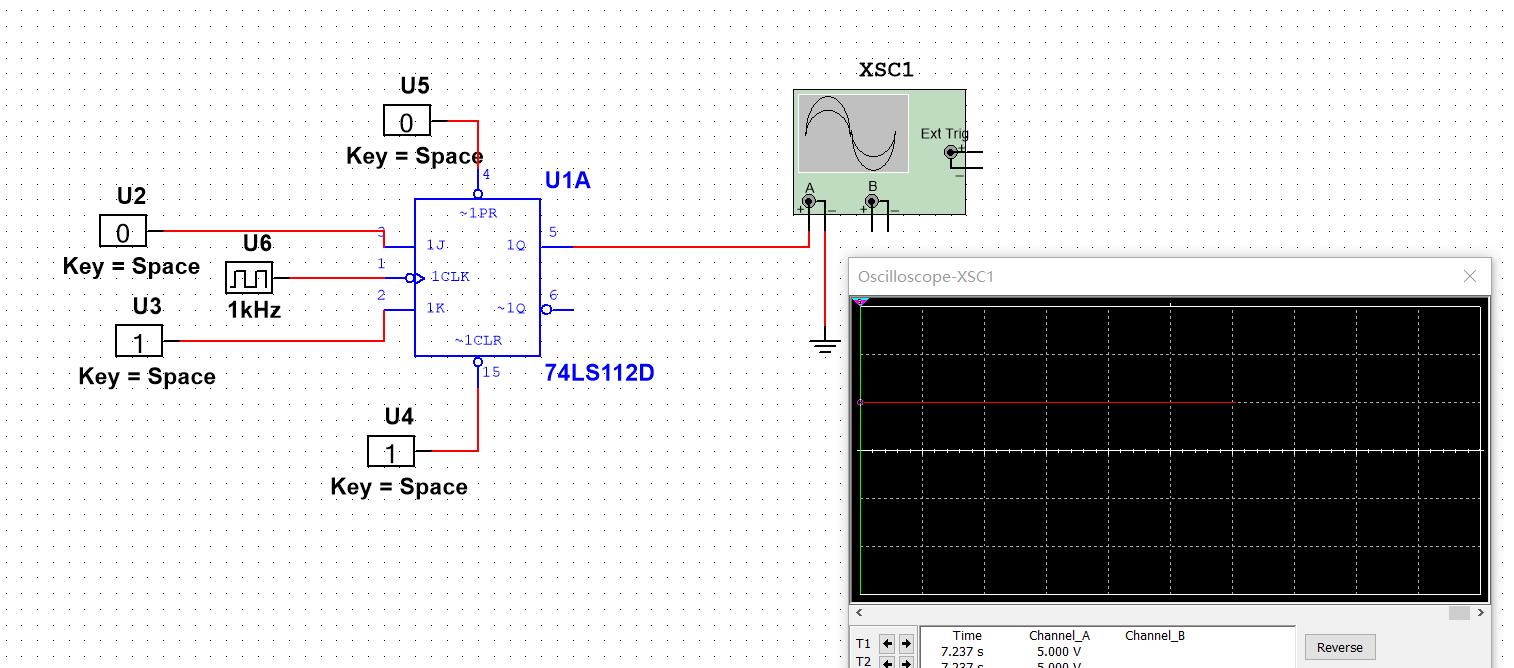


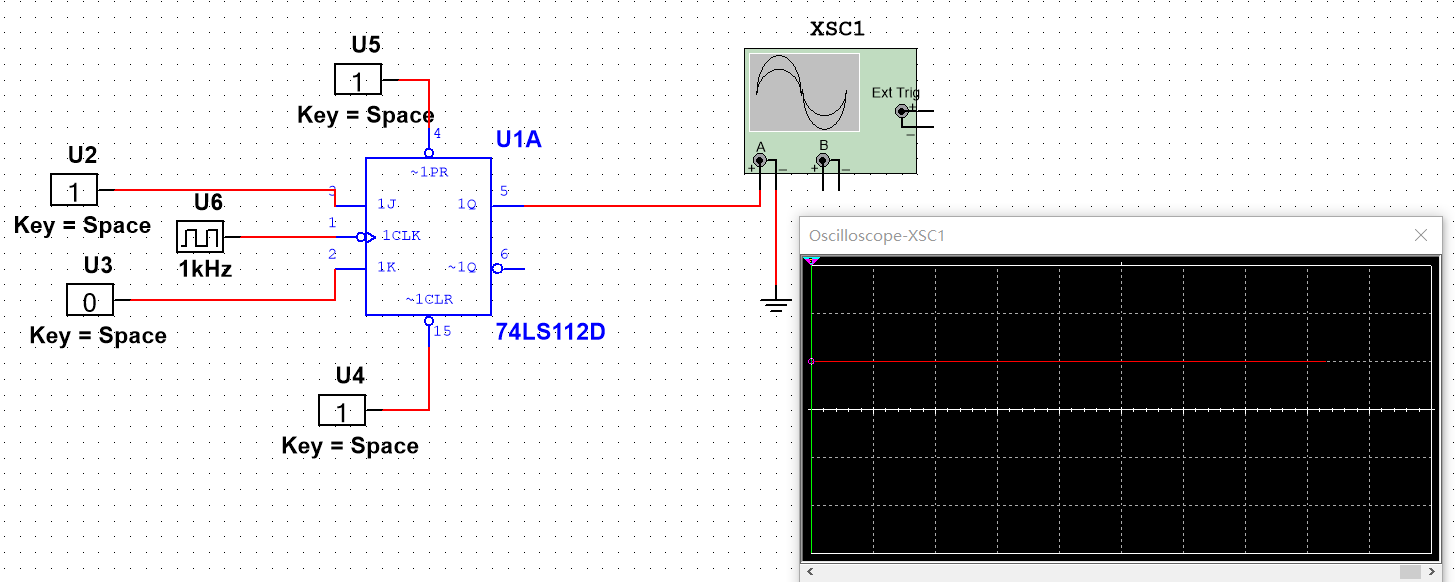


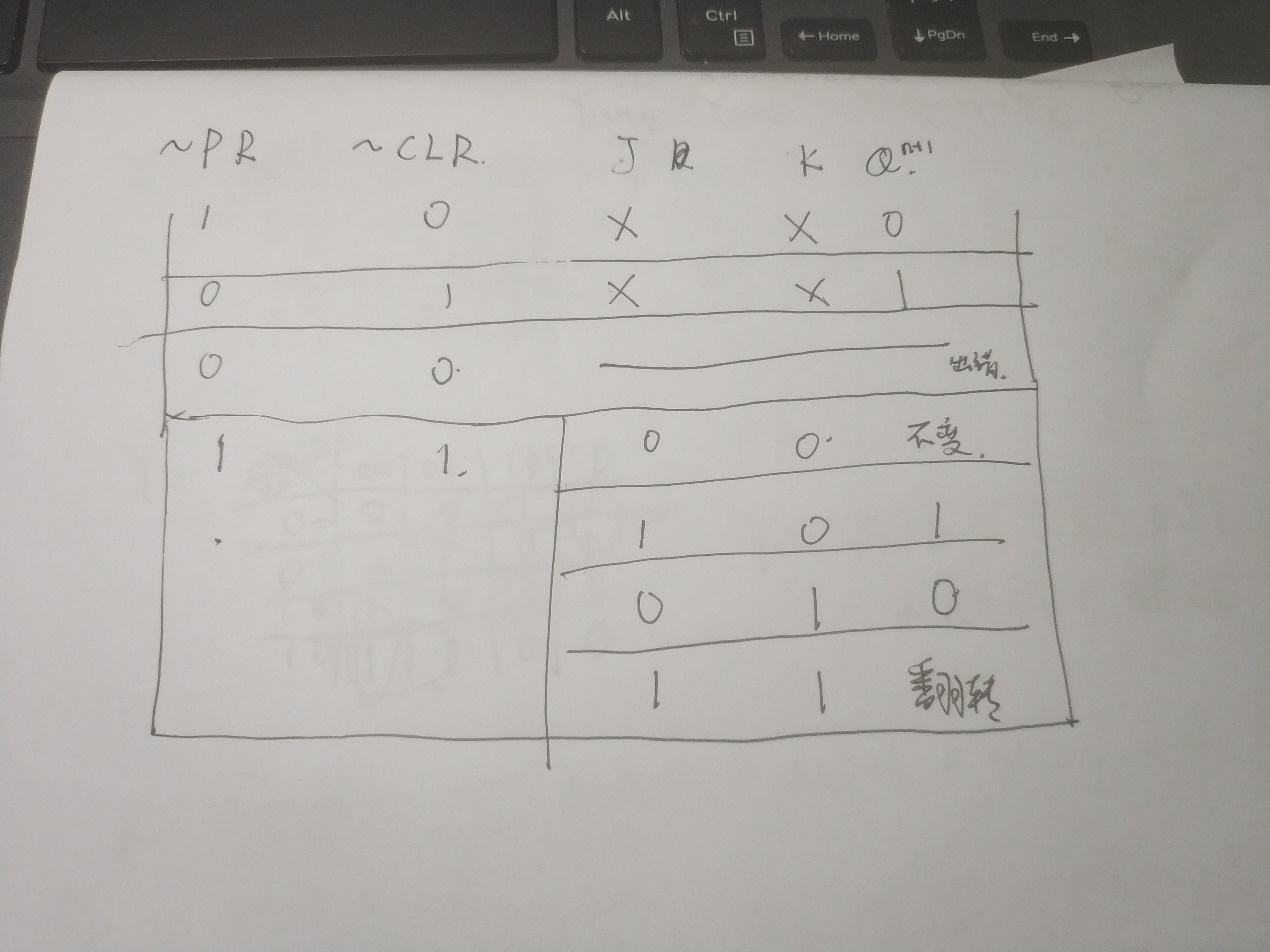
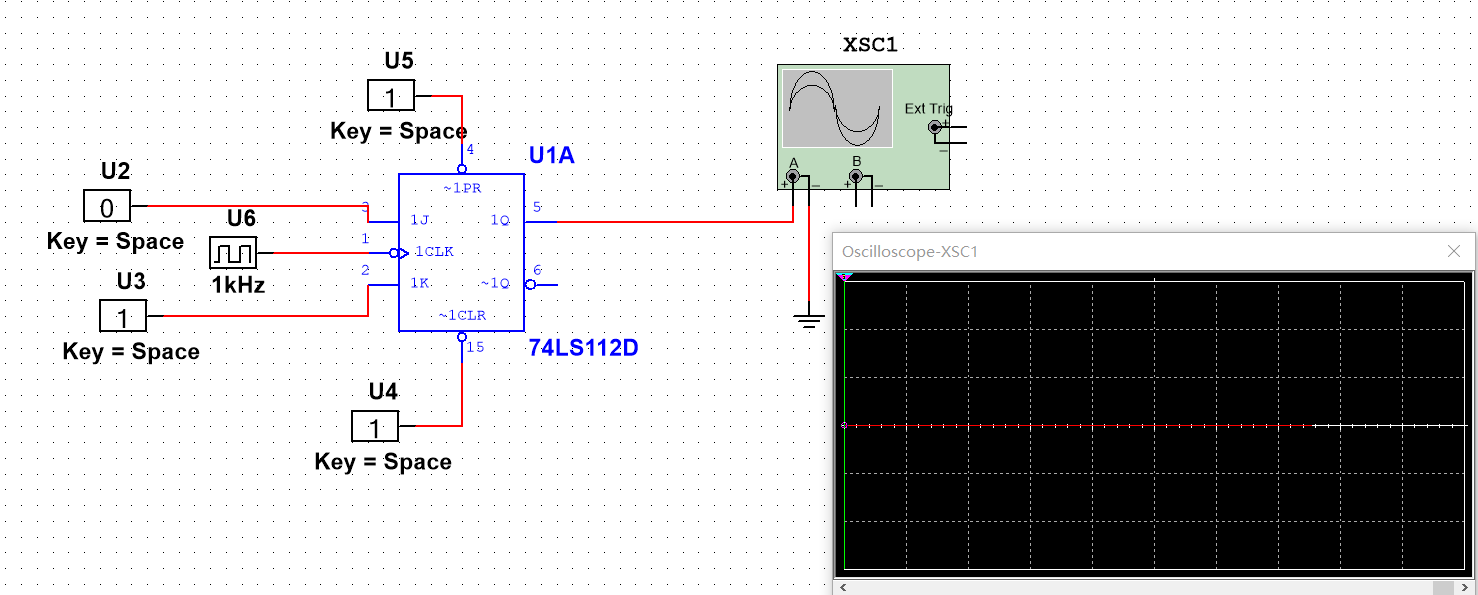
2、测试集成JK触发器（74LS112）和D触发器（74LS74）及用它们接成的T′触发器的逻辑功能和触发方式

JK触发器

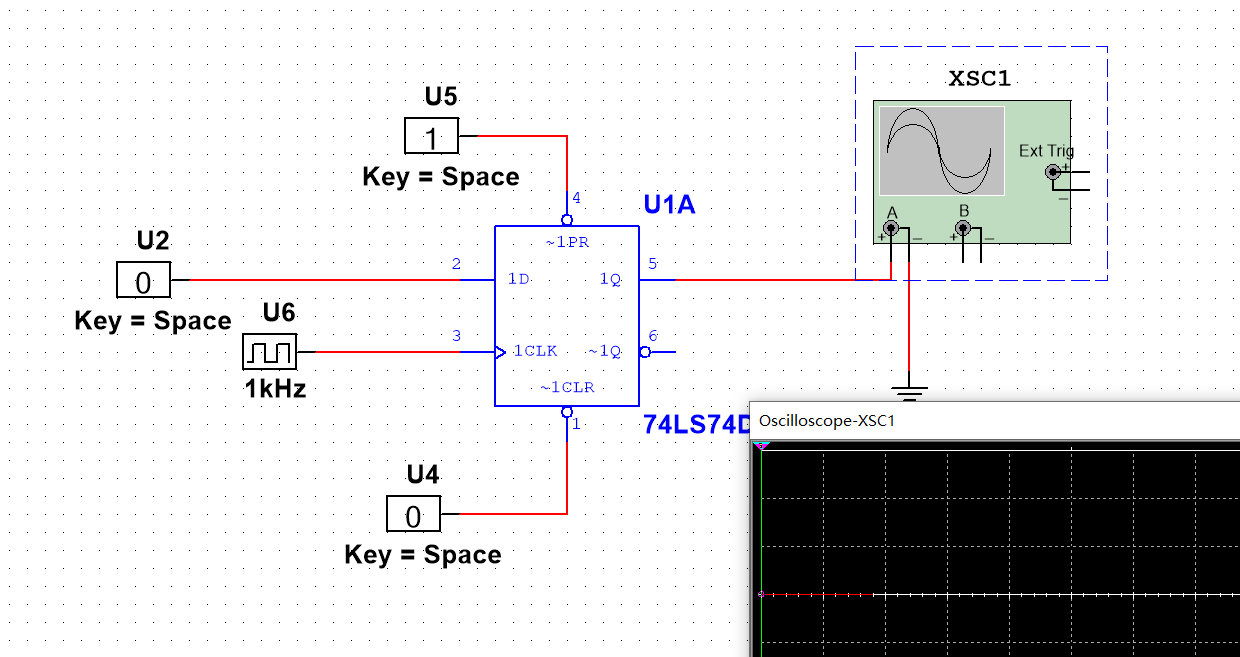


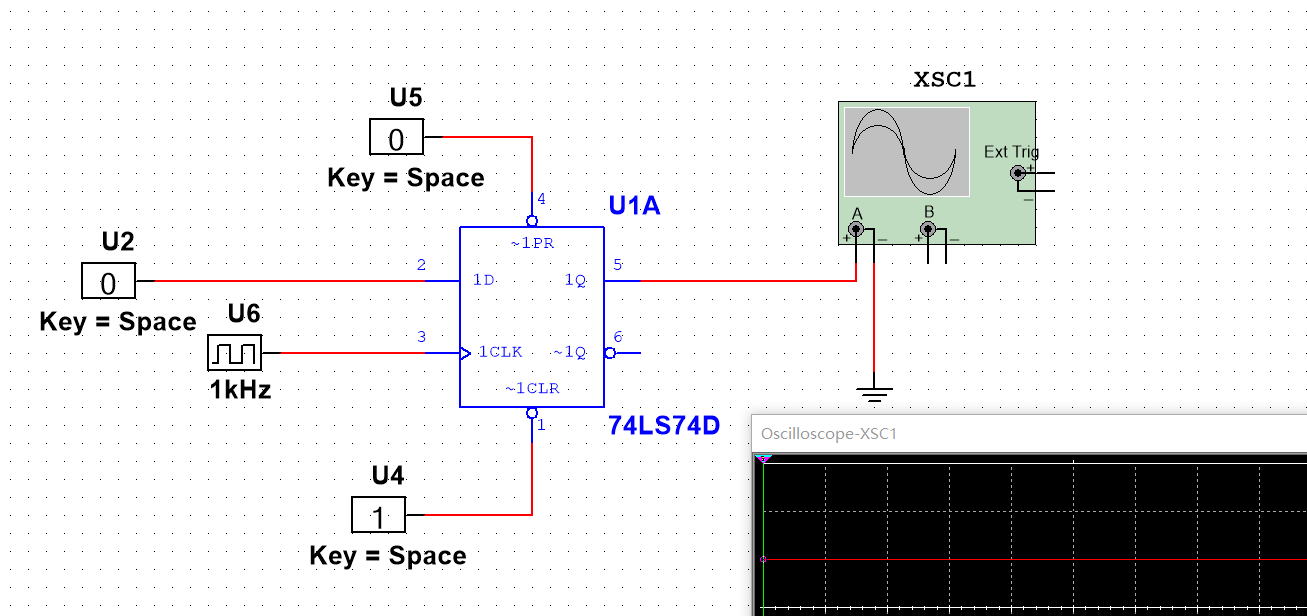


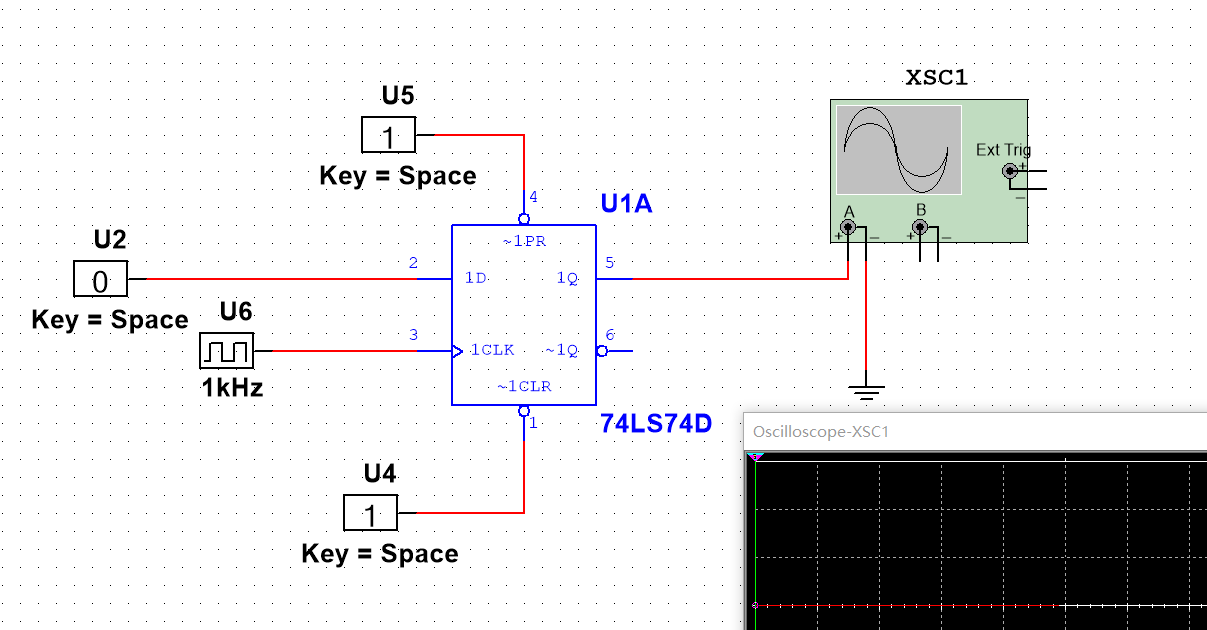


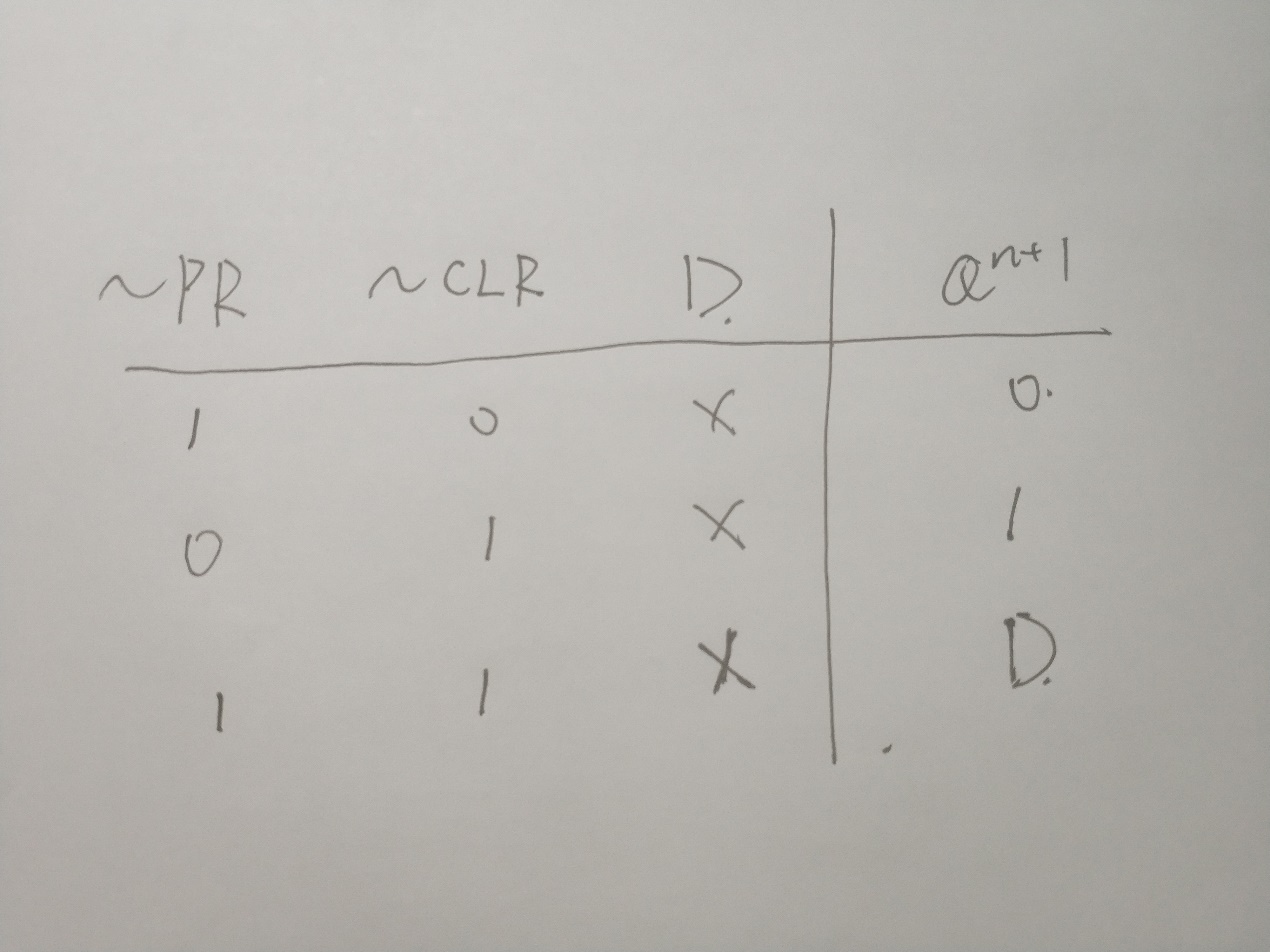
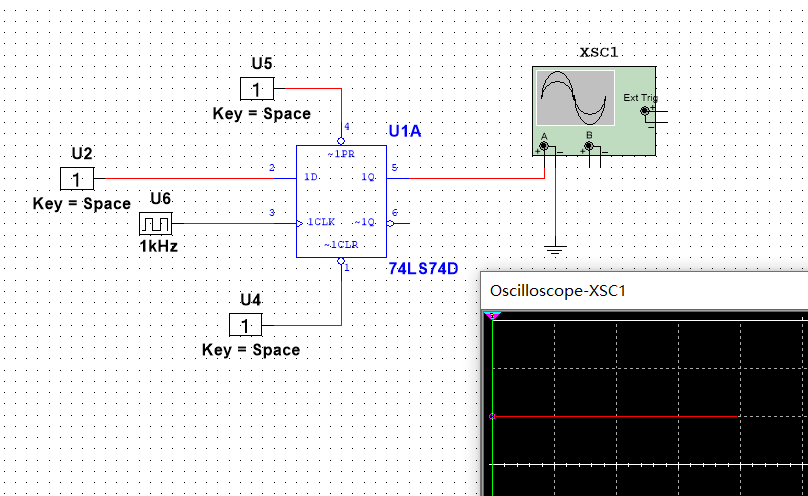


D触发器









T′触发器

用D触发器可直接构成T’触发器

